



# **SCHEME & SYLLABUS OF UNDERGRADUATE DEGREE COURSE**

## **Electrical and Electronic Engineering**

### **V Semester**



**Effective for the students admitted in year 2021-22 and onwards.**



**Teaching and Examination Scheme 3<sup>rd</sup> Year – V Semester**

<b>THEORY</b>											
S. No.	Category	Code	Course Title	Contact hrs./week			Marks				Cr
				L	T	P	Exam Hrs.	IA	ETE	Total	
1	DC	5EX4-01	Power System-I	3	0	0	3	30	70	100	3
2	DC	5EX4-02	Computer Architecture and Microprocessors	3	0	0	3	30	70	100	3
3	DC	5EX4-03	Control System Engineering	3	0	0	3	30	70	100	3
4	DC	5EX4-04	Digital Electronics	3	0	0	3	30	70	100	3
5	DE-1	5EX5-11	High Voltage Engineering	2	0	0	2	30	70	100	2
		5EX5-12	Optimisation Techniques								
		5EX5-13	Introduction to VLSI								
6	DE-2	5EX5-14	Engineering Materials	2	0	0	2	30	70	100	2
		5EX5-15	Fundamentals of Communication Systems								
		5EX5-16	Energy Conversion and Auditing								
<b>Sub Total</b>				<b>16</b>	<b>0</b>	<b>0</b>	<b>-</b>	<b>180</b>	<b>420</b>	<b>600</b>	<b>16</b>
<b>PRACTICAL &amp; SESSIONAL</b>											
7	DC	5EX4-20	MATLAB Programming Lab	0	0	3		60	40	100	1.5
8	DC	5EX4-21	Microprocessors Lab	0	0	3		60	40	100	1.5
9	DC	5EX4-22	Control System Engineering Lab	0	0	3		60	40	100	1.5
10	DC	5EX4-23	Digital Electronics Lab	0	0	3		60	40	100	1.5
11	UI	5EX7-30	Industrial Training (45 days)	0	0	6		60	40	100	3
12	UGE	5EX8-00	Co-Curricular Activities	0	0	2		60	40	100	1
<b>Sub- Total</b>				<b>0</b>	<b>0</b>	<b>20</b>		<b>360</b>	<b>240</b>	<b>600</b>	<b>10</b>
<b>TOTAL OF V SEMESTER</b>				<b>16</b>	<b>0</b>	<b>20</b>		<b>540</b>	<b>660</b>	<b>1200</b>	<b>26</b>

**Syllabus****B.Tech. (Electrical and Electronics Engineering)****III Year V Semester**

<b>5EX4-01: Power System-I</b>	
<b>Credit:3</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>3L+0T+ 0P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Understand the basics of supply systems, requirement of conductor material and effect of voltage on size of conductor.

**CO-2:** Calculate the sag and tension of overhead transmission lines and also know about the effect of wind and ice loading, conductor vibrations and vibration dampers.

**CO-3:** Evaluate the electrical parameters like resistance, inductance and capacitance of overhead lines and also know about the Skin, Proximity effects and Corona effect.

**CO-4:** Know about the various types of insulators and underground cables.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2.	<b>Supply and Distribution Systems:</b> Basic network topology of power system. Transmission and distribution voltage, effect of system voltage on size of conductor and losses. Comparison of supply systems. Transmission and Distribution Systems: Line diagrams, transmission and distribution voltage levels, Kelvin's law for conductor size.	7
3.	<b>Mechanical Parameters of Overhead Lines:</b> Conductor material and types of conductor. Conductor arrangements and spacing. Calculation of sag and tension, supports at different levels, effect of wind and ice loading, stringing chart and sag template. Conductor vibrations and vibration dampers.	8
4.	<b>Electrical Parameters of Overhead Lines:</b> Resistance, inductance and capacitance of overhead lines, effect line transposition. Geometric mean radius and distance. Inductance and capacitance of line with symmetrical and unsymmetrical spacing, Inductance and capacitance of double circuit lines. Skin and Proximity effects. Equivalent circuits and performance of short and medium transmission lines.	9
5.	<b>Transmission Line Parameters:</b> Equivalent circuit of transmission lines, Ferranti effect. Interference with communication circuits. Corona: Electric stress between parallel conductors. Disruptive critical voltage and visual critical voltage, Factors affecting corona. Corona power loss. Effects of corona.	8
6.	<b>Insulators and Underground Cables:</b> Insulators: Pin, shackle, suspension, post and strain insulator. Insulator string, string efficiency, grading and methods of improving string efficiency. Types of underground cable, Materials for conductor, insulator, sheathing and armoring. Insulator resistance and capacitance calculation. Electrostatic stresses and reduction of maximum stresses. Causes of breakdown. Thermal	8



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	rating of cable.Introduction to oil filled and gas filled cables.	
	<b>Total</b>	<b>41</b>
<b>Suggested Books:</b> <ol style="list-style-type: none"><li>1. C. L. Wadhwa, Electrical Power Systems, New Age Publications.</li><li>2. Nagrath, Kothari, Modern Power System Analysis, McGraw Hill Education.</li><li>3. Ashfaq Hussain, Electrical Power System, CBS Publisher.</li><li>4. Soni, Gupta and Bhatnagar: A Course in Electrical Power, Dhanpat Rai.</li><li>5. B. R. Gupta: Power System Analysis &amp; Design, S. Chand Publishers.</li><li>6. A. S. Pabla: Electric Power Distribution, McGraw Hill Education.</li></ol>		

**III Year V Semester**

<b>5EX4-02: Computer Architecture and Microprocessors</b>	
<b>Credit:3</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>3L+0T+ 0P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** To understand the architecture and working of the microprocessor.

**CO-2:** To write the assembly language programming,

**CO-3:** To understand the overview of computer organization.

**CO-4:** To understand the principle of CPU system.

**CO-5:** To understand the principle of memory system

**CO-6:** To explain the principle of data flow.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>1</b>
2.	Fundamental of Microprocessor: Introduction to Microprocessors, Microprocessor systems with bus organization, Microprocessor architecture and operation, 8085 Microprocessor and its operation, 8085 instruction cycle, machine cycle, T states, Addressing modes in 8085, Introduction to 8086.	<b>10</b>
3	Introduction to Assembly Language Programming: Assembly Language Programming Basics, Classification of Instructions and Addressing Mode, 8085 Instruction Sets, Assembling, Executing and Debugging the Programs, Developing Counters and Time Delay Routines, Interfacing Concepts	<b>9</b>
4.	Basic Computer Architecture: <b>Introduction:</b> History of Computer architecture, Overview of computer organization, Memory Hierarchy and cache, Organization of hard disk.  <b>Instruction Codes:</b> Stored Program Organization-Indirect Address, Computer Registers, Common bus system, Instruction set, Timing and Control-Instruction Cycle.	<b>4</b>
5.	Micro-programmed Control: Basic Computer Design of Accumulator: Control of Ac Register, ALU Organization; Control Memory-Address Sequencing; Conditional Branching, Mapping of Instruction-Subroutines; Micro Program: Symbolic Micro Program, Binary Micro Program;  Design of Control Unit: Basic Requirement of Control Unit, Structure of Control Unit, Micro Program Sequencer.	<b>8</b>



6.	Central Processing Unit: <b>General Register Organization:</b> Control Word, Stack Organization and Instruction; Formats-Addressing Models.  <b>Data Transfer and Manipulation:</b> Data Transfer Instruction, Data Manipulation Instructions, Arithmetic Instructions, Logical and Bit Manipulation Instruction, Shift Instructions.	<b>8</b>
<b>Total</b>		<b>40</b>
<b>Suggested Books:</b> <ol style="list-style-type: none"><li>1. Ramesh S. Gaonkar: Microprocessor Architecture, Programming, and Applications with 8085, prentice Hall</li><li>2. Morris Mano: Computer system Architecture, Third Edition, prentice Hall</li><li>3. Malvino: Digital Computer system Electronics (An introduction to Microcomputers)</li><li>4. Douglas V. Hall: Microprocessor and Interfacing programming and Hardware, McGraw Hill.</li><li>5. Computer Organization and Architecture, William Stallings, 9th Edition, Pearson.</li></ol>		

**III Year V Semester**

<b>5EX4-03: Control System Engineering</b>	
<b>Credit:3</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>3L+0T+ 0P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Understand the concept of control systems and their types. Representation of control systems by block diagram and signal flow graph.

**CO-2:** Learn the importance of control systems and their transient analysis along with their design specifications. Also able to apply Laplace Transform for evaluation of time response.

**CO-3:** Know the concept of stability and its determination through Routh-Hurwitz stability criteria and Root Locus.

**CO-4:** Find the frequency response of a system through Polar plot, Nyquist plot and Bode plots.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>1</b>
2.	<b>Introduction of Control Systems:</b> Concept of open loop and closed loop control systems, Examples and applications of open loop and closed loop systems, Elements of control systems, Differential equations representation of Electro-Mechanical systems, Transfer function calculation by block diagram reduction techniques and signal flow method.	<b>9</b>
3.	<b>Laplace Transform and Transient Analysis of Control System:</b> Laplace Transformation, inverse Laplace transformation, Application of initial and final value theorem, Time response of first and second-order systems with impulse, step, ramp and parabolic inputs, Design specifications for second-order systems, Order, type and characteristics equation of control systems.	<b>9</b>
4.	<b>Error Analysis and Stability of Control System:</b> Steady state errors and error constants, Transient and steady state analysis of control systems, concept of stability and necessary conditions, Routh-Hurwitz stability criteria and limitations. Stability analysis through Root Locus Technique.	<b>8</b>
5.	<b>Control System Components and Controllers:</b> AC servomotor, synchronous and stepper motor. Application of Proportional, Integral and Derivative Controllers, Lead, Lag and Lead-Lag compensators.	<b>7</b>
6.	<b>Frequency Response Analysis:</b> Correlation between time and frequency responses, Polar plot, Nyquist plot and Nyquist stability criterion. Bode plots, Gain Margin and Phase Margin.	<b>7</b>
<b>Total</b>		<b>41</b>





**Suggested Books:**

1. I. J. Nagrath and M. Gopal: Control Systems Engineering, New AgePublication.
2. K. Ogata: Modern Control Engineering, Prentice Hall of India.
3. Benjamin C. Kuo, Automatic Control Systems, Wiley India.
4. A.K. Jairath, Problems and Solutions of Control Systems, CBS Publishers.



**III Year V Semester**

<b>5EX4-04: Digital Electronics</b>	
<b>Credit:3</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>3L+0T+ 0P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Understand the basics of number systems and logic gates.

**CO-2:** Explain finite state model and minimization techniques

**CO-3:** Know structure and design of combinational and sequential logic circuits.

**CO-4:** Understand the concept different logic families.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, Scope and Outcome of the course	1
2.	<b>Number System, Codes and Logic Gates:</b> Arithmetic of Nonconventional Number System, Weighted Codes, Binary codes, Code Conversion, Error Correction/Detection Codes, BCD codes, Fixed point & floating point Number System. Basic, Exclusive and Universal Gates, Hazardous in the circuits.	8
3.	<b>Logic Simplification and Minimization Techniques:</b> Review of Boolean Algebra and De Morgan's Theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, Tabulation Method.	7
4.	<b>Combinational Logic Circuits Design:</b> Half and Full Adders, Subtractors, Serial and Parallel Adders, BCD Adder, Magnitude Comparators, Multiplexers, Encoder, Decoder, Driver & Multiplexed Display, Logic Implementation using combination blocks.	8
5.	<b>Sequential Logic Circuits Design:</b> Building blocks like S-R, JK and Master-Slave JK FF, Edge triggered FF, Ripple and Synchronous counters, Shift registers, Finite state machines, Design of Synchronous FSM, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator, Clock generation	8
6.	<b>Logic Families and PLD Concept:</b> TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing. Basics of HDL (VHDL/Verilog), Syntax and Semantics of HDL. Concept of Programmable logic devices like FPGA. Logic implementation using programmable devices.	8
<b>Total</b>		<b>40</b>



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**Suggested Books:**

1. M. Morris Mano: Digital Design, Third Edition, Prentice Hall
2. R. P. Jain: Modern Digital Electronics, Third Edition, TMH
3. Taub and Schilling: Digital Integrated Electronics, McGRAW HILL
4. Sandige: Digital concept Using standard ICs
5. R. J. Tocci: Digital Systems: Principles and Applications, Fourth Edition, Prentice Hall

**III Year V Semester**

<b>5EX5-11: High Voltage Engineering</b>	
<b>Credit:2</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>2L+0T+ 0P</b>	<b>EndTermExams:2 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** understand the process of generation of high voltage DC and AC.

**CO-2:** know about the Over voltages and Travelling Waves and their causes and effects.

**CO-3:** Learn about the breakdown in gases, liquids and solids and insulation tests.

**CO-4:** Analyze the protection scheme of the high voltage equipments like surge absorber, rod-gap, arcing horn and lighting arresters.

<b>S. No.</b>	<b>Contents</b>	<b>Hours</b>
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>1</b>
2.	<b>High Voltage DC and AC Generation:</b> Generation of high voltage DC, basic voltage multiplier circuit, High voltage AC generation through Cascaded Transformers. <b>Impulse Voltage generation:</b> Impulse voltage, basic impulse circuit, Mark's multistage impulse generator, Construction and operation of Sphere-gap.	<b>8</b>
3.	<b>Over voltages and Travelling Waves:</b> Causes of over-voltages, introduction to lightning phenomena, over-voltages due to lightning. Travelling waves on transmission lines-open end line, short circuited line, line terminated through a resistance, line connected to a cable, reflection and refraction at a T-junction and line terminated through a capacitance. Attenuation of traveling waves.	<b>8</b>
4.	<b>Breakdown in Gases, Liquids and Solids:</b> Introduction to mechanism of breakdown in gases, Townsend's breakdown mechanism. Breakdown in electromagnetic gases, Application of gases in power system. Introduction to mechanism of breakdown in liquids, suspended solid particle mechanism and cavity breakdown. Application of oil in power apparatus. Introduction to mechanism of breakdown in solids, electromechanical breakdown, treeing and tracking breakdown and thermal breakdown.	<b>8</b>
5.	<b>Insulation Tests:</b> Measurement of resistivity, dielectric constant and loss factor. High Voltage Schering Bridge- measurement of capacitance and dielectric loss. Introduction to partial discharge, partial discharge equivalent circuit. Basic wide-band and narrow band PD detection circuits.	<b>8</b>
6.	<b>Over Voltage Protection:</b> Basic construction and operation of ground wires protection angle and protective zone, ground rods, counterpoise, surge absorber, rod-gap and arcing horn, lighting arresters - expulsion type, non-linear gap type and metal oxide gapless type. Introduction of Insulation Coordination.	<b>8</b>
<b>Total</b>		<b>41</b>



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**Suggested Books:**

1. Naidu: High Voltage Engineering, MGH.
2. C. L. Wadhwa: High Voltage Engineering, Wiley Eastern Ltd.
3. Ravindra Arora, Bharat Singh Rajpurohit: Fundamentals of High Voltage Engineering, Wiley.
4. Subir Ray: An Introduction to High Voltage Engineering, Prentice Hall of India.

**III Year V Semester**

<b>5EX5-12: Optimization Techniques</b>	
<b>Credit:2</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>2L+0T+ 0P</b>	<b>EndTermExams:2 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** To study the concept of optimization techniques and their classification.

**CO-2:** To study the Linear programming concepts and able for problem solving using various LP methods.

**CO-3:** To study and understand Queuing models and distributions.

**CO-4:** Define and explain the different statistical distributions like Binomial, Poisson, Normal, Uniform, and Exponential distributions and compute the method of least squares, correlation and regression.

**CO-5:** To study and understand the Unconstrained Optimization methods.

**CO-6:** To study and understand the Constrained Optimization methods.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>1</b>
2.	<b>INTRODUCTION:</b> Introduction to Optimization: Engineering application of Optimization – Statement of an Optimization problem – Optimal Problem formulation – Classification of Optimization problem.	<b>7</b>
3.	<b>LINEAR PROGRAMMING:</b> Examples of linear programming problems – formulation simplex methods variable with upper bounds – principle- duality - dual simplex method - sensitivity analysis – revised simplex procedure – solution of the transportation problem – assignment – network minimization – shortest route problem – maximal two problem – L.P. representation of networks.	<b>10</b>
4.	<b>QUEUING THEORY:</b> Queuing Models, classification of queuing models, probability distribution in queuing systems, poisson and exponential distributions -Queues with combined arrivals and departures-random and series queues.	<b>6</b>
5.	<b>UNCONSTRAINED OPTIMIZATION:</b> Maximization and minimization of convex functions. Necessary and sufficient conditions for local minima – speed and order of convergence – univariate search – steepest and descent methods-metcher reeves method -conjugate gradient method.	<b>9</b>
6.	<b>CONSTRAINED OPTIMIZATION:</b> Necessary and sufficient condition – equality constraints, inequality constraints -kuhu – tucker conditions – gradient projection method – penalty function methods – cutting plane methods of sibel directions.	<b>8</b>
<b>Total</b>		<b>41</b>



**Suggested Books:**

1. Rao S.S, “Optimization – Theory and applications”, Wiley Easter Ltd., 1979
2. Hadley G. “Nonlinear and – dynamic programming” Addison Wesley Publishing Co. 1964.
3. Cordan C.C. Beveridge and Robert S. Schedther, “Optimization, Theory and Practice” McGraw Hill Co.1970.
- 4.HarndyA.Tahh. “Operations Research, An Introduction”, Macmillan Publishers Co.NewYork, 1982.
5. Beightferand S., “Foundations of Optimization Pill”, New Delhi, 1979.

**III Year V Semester**

<b>5EX5-13: Introduction to VLSI</b>		
<b>Credit:2</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>	
<b>2L+0T+ 0P</b>	<b>EndTermExams:2 hrs.</b>	
<b>Course Outcomes:</b> Upon successful completion of the course, the students will be able to: <b>CO-1:</b> To study the MOS transistors and understand MOS fabrication techniques. <b>CO-2:</b> To study the NMOS and CMOS Inverter design concepts. <b>CO-3:</b> To understand various CMOS logic circuits and their working. <b>CO-4:</b> To design simple CMOS logic circuits. <b>CO-5:</b> To study VHDL and coding for sequential circuits.		
<b>S. No.</b>	<b>Contents</b>	<b>Hours</b>
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>01</b>
2.	<b>Introduction to MOS Technology:</b> Basic MOS transistors, Enhancement Mode transistor action, Depletion Mode transistor action, NMOS and CMOS fabrication.	<b>08</b>
3.	<b>Basic Electrical Properties of MOS Circuits:</b> IDS versus VDS relationship, Aspects of threshold voltage, Transistor Trans conductance. The NMOS inverter, Pull up to Pull-down ratio for a NMOS Inverter and CMOS Inverter (Bn/Bp), MOS transistor circuit Model, Noise Margin.	<b>08</b>
4.	<b>CMOS Logic Circuits:</b> The inverter, Combinational Logic, NAND Gate NOR gate, Compound Gates, 2input CMOS Multiplexer, Memory latches and registers Transmission Gate, Gate delays, CMOS-Gate Transistor sizing, Power dissipation	<b>08</b>
5.	<b>Basic Physical Design of Simple Gates and Layout Issues:</b> Layout issues for inverter, Layout for NAND and NOR Gates, Complex Logic gates Layout, Layout optimization for performance.	<b>08</b>
6.	<b>Introduction to VHDL:</b> Verilog and other design tools. VHDL Code for simple Logic gates, flip-flops, shift-registers, Counters, Multiplexers, adders and subtractors.	<b>07</b>
<b>Total</b>		<b>40</b>
<b>Suggested Books:</b> 1. S M Sze: VLSI Technology (TMH) 2. SM KANG:CMOS Digital Integrated Circuits, TMH 3. Stephen A Compbell: The Science & Engineering of Microelectronic Fabrication Oxford. 4. James D Plummer, Micheal Deal & Petter B Griffin: Silicon VLSI Tech. Fundamental, Practice & Modeling, Prentice Hall. .		





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**III Year V Semester**

<b>5EX5-14: Engineering Materials</b>	
<b>Credit:2</b>	<b>Max Marks:100(IA: 30,ETE: 70)</b>
<b>2L+0T+ 0P</b>	<b>EndTermExams:2 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Know about the basic concepts of the materials and their bonds.

**CO-2:** Understand the various properties of the conducting materials, free electron theory of metals, electrical and thermal conductivity of metals

**CO-3:** Apply the knowledge of semiconductor materials and relevant concepts.

**CO-4:** Define and explain the different magnetic materials and their applications. Also able to explain the superconductivity and zero resistance.

**CO-5:** Explain the dielectric properties of insulators, piezoelectricity, frequency dependence of electronic and ionic polarizability and dielectric losses.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2.	<b>Concepts of Materials Science:</b> Ionic, covalent, metallic and molecular bindings-Bond angle, bond length and bond energy, Bonding and types of solids, Crystalline state and their defects, Classical theory of electrical and thermal conduction in solids, temperature dependence of resistivity.	8
3.	<b>Conducting Materials:</b> Conductivity of metals, Free electron theory of metals Ohm's law and relaxation time of electrons, collision time and mean free path, electron scattering and resistivity of metals. Electrical and thermal conductivity of metals.	8
4.	<b>Semiconductor Materials:</b> Classification of semiconductors, semiconductor conductivity, temperature dependence, Carrier density and energy gap, Trends in semiconductor materials used in electrical equipment.	7
5.	<b>Magnetic Properties of materials and Superconductivity:</b> Magnetization of matter, Magnetic Material Classification, Magnetic properties – Dia-magnetism Para-magnetism, Ferro-magnetism, Ferri-magnetism and Antiferro-magnetism, Curie-Weiss Law, Soft and Hard Magnetic Materials, Superconductivity and its origin, Zero resistance and Meissner Effect.	9
6.	<b>Dielectric Properties of Insulators:</b> Dielectric Properties in Static and Alternating fields, Dielectric constant of mono-atomic gases, poly-atomic molecules and solids, Internal field in solids and liquids, Properties of Ferro-Electric materials, Polarization, Piezoelectricity, Frequency dependence of Electronic and Ionic Polarizability, Complex dielectric constant of non-dipolar solids, dielectric losses.	8
<b>Total</b>		<b>41</b>



**Suggested Books:**

1. Introduction to Materials Science and Engineering, William J Callister, John Wiley & Sons, Inc.
2. W. D. Callister, "Materials Science and Engineering: An Introduction", John Wiley & Sons.
3. V. Raghavan, "Materials Science and Engineering: A First Course", Prentice Hall

**III Year V Semester****5EX5-15: Fundamentals of Communication Systems****Credit:2****Max Marks:100(IA: 30,ETE: 70)****2L+0T+ 0P****EndTermExams:2 hrs.****Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Analyze the power and transmission bandwidth of Amplitude and Frequency Modulated signals.**CO-2:** Familiarize the process of reproduction of base band signal.**CO-3:** Analyze various pulse analog and pulse digital Modulation Techniques.**CO-4:** Understand the transmission of binary data in communication systems.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	<b>1</b>
2.	<b>Amplitude Modulation:</b> Introduction to Modulation, Need for Modulation, Ordinary Amplitude Modulation – Modulation index, Side bands, AM Power, Double Side Band Suppressed Carrier Modulation, Single Side Band Modulation, Vestigial Side Band Modulation, AM demodulation, Applications of AM.	<b>10</b>
3.	<b>Angle Modulation:</b> Angle Modulation fundamentals, Frequency Modulation – Modulation index and sidebands, Narrowband FM, Wideband FM, Principles of Phase Modulation, Frequency Modulation verses Amplitude Modulation, FM demodulation, Frequency Division Multiplexing, Applications of FM.	<b>9</b>
4.	<b>Signal Sampling and Analog Pulse Communication:</b> Ideal Sampling, Pulse Amplitude Modulation, Pulse Width Modulation, Pulse Position Modulation. <b>Digital Communication Techniques:</b> Quantization, Digital Transmission of Data, Parallel and Serial Transmission, Data Conversion, Time Division Multiplexing, Pulse Code Modulation, Delta Modulation.	<b>10</b>
5.	<b>Transmission of Binary Data in Communication Systems:</b> Digital Codes, Principles of Digital Transmission, Transmission Efficiency, Modem Concepts and Methods – FSK, BPSK, Error Detection and Correction.	<b>10</b>
<b>Total</b>		<b>40</b>

**Suggested Books:**

1. George Kennedy, Bernard Davis, S. R. M Prasanna, Kennedy's Electronic Communication System, McGraw Hill , 6th Edition 2017
2. S Haykins, Communications Systems, Wiley , 4th Edition 2006
3. Wayne Tomasi, Electronic Communication Systems, 5th Edition, Pearson Education.
4. B.P.Lathi, "Modern Digital and Analog Communication Systems", 4th Edition, Oxford University Press, 2011.



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5. D.Roody, J.Coolen, Electronic Communications, 4th edition, PHI, 2006.
6. B.Sklar, "Digital Communications Fundamentals and Applications", 2nd Edition Pearson Education, 2007.
7. H P Hsu, Schaum Outline Series - "Analog and Digital Communications" TMH, 2006

**III Year V Semester****5EX5-16: Energy Conversion and Auditing****Credit:2****Max Marks:100(IA: 30,ETE: 70)****2L+0T+ 0P****EndTermExams:2 hrs.****Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Explain the basic principles of energy conversion, Energy in magnetic systems, energy and co-energy and single excited systems.

**CO-2:** Know about the energy conversion systems like solar to thermal, solar to electrical, wind to electrical energy and OTEC and bio-mass.

**CO-3:** Define **energy management**, energy audit and its need, types of energy audit and energy audit instruments.

**CO-4:** Implement the energy efficient devices, automatic power factor controllers, maximum demand controllers, energy efficient motors and soft starters with energy saver.

S. No.	Contents	Hours
1.	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2.	<b>Principals of Energy Conversion:</b> Introduction, Flow of energy in electromechanical devices, Energy in magnetic systems, energy and co-energy, single excited systems: determination of force, mechanical energy, torque equations.	8
3.	<b>Conversion of Renewable Energy:</b> Introduction to world energy scenario, renewable energy resources, Solar energy: solar to thermal energy, solar to electrical energy, solar photovoltaic system, Wind energy: basic principles of wind energy conversion, basic components of wind energy conversion system, Introduction to Ocean Thermal Energy Conversion (OTEC), Bio Mass energy conversion.	8
4.	<b>Energy Management and Audit:</b> Energy audit and its need, types of energy audit. Energy management, understanding energy costs, optimizing the input energy requirements, energy audit instruments. Material and Energy balance: Facility as an energy system, methods for preparing process flow, material and energy balance diagrams.	7
5.	<b>Energy Efficiency in Electrical Systems:</b> Electricity billing, electrical load management and maximum demand control, power factor improvement and its benefits, selection and location of capacitors, performance assessment of PF capacitors, distribution and transformer losses. Electric motors: Types, losses in induction motors, motor efficiency, factors affecting motor performance.	9
6.	<b>Energy Efficient Technologies in Electrical Systems:</b> Automatic power factor controllers, Maximum demand controllers, energy efficient motors, soft starters with energy saver, variable speed drives, energy efficient transformers, electronic ballast, occupancy sensors, energy efficient lighting controls, energy saving potential of each technology.	8



Total	41
<b>Suggested Books:</b> <ol style="list-style-type: none"><li>1. G.D.Rai, Non-Conventional Energy Sources, Khanna Publishers.</li><li>2. P.C. Sen, Principles of Electric Machines and Power Electronics, Wiley India Pvt. Ltd.</li><li>3. Mehmet Kanoğlu, Yunus A. Çengel, “Energy Efficiency and Management for Engineers”, McGraw-Hill Education.</li></ol>	



**III Year V Semester****5EX4-20: MATLAB Programming Lab****Credit:1.5****Max Marks:100(IA:60,ETE: 40)****0L+0T+3P****EndTermExams:3 hrs.****Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Write scripts and function in MATLAB to perform mathematical operations on matrixes, arrays and constants.

**CO-2:** Draw the responses of any kind of system using plot features of MATLAB.

**CO-3:** Demonstrate the loop, if-else, control flow, break-point operations.

**CO-4:** Simulate a model in Simulink to get the desired response.

S. No.	List of Experiments	Hours
1.	Introduction to MATLAB, its various tools and files.	3
2.	Create matrixes, vectors, array, multi-dimensional matrixes and their operations through script file in MATLAB (one example of each).	3
3.	Scripts and functions; Global Variables; Open, saving and loading data; Debugging of scripts.	3
4.	Create script files demonstrating plot and sub-plot of simple graphs and their editing through figure editor tools. Perform label, title, legend, axis, zoom-in, zoom-out etc. operations.	3
5.	Demonstrate Loops, Advanced data objects, Break-point and Structures by writing script files. (Through tutorial sheets)	3
6.	Demonstrate If-else, Branches and Control flow through writing example script files. (Through tutorial sheets)	3
7.	Applications: linear algebra, curve fitting, interpolation, Numerical integration, Ordinary differential equation. (Cover through tutorial sheets)	3
8.	Simulink: Basics of Simulink, Problems based on Simulink. Draw and simulate a Simulink model of a transfer function and get its step and ramp responses.	3
9.	Implement a PID controller and perform its tuning through PID controller tuning toolbox.	3
<b>Total</b>		<b>27</b>

**III Year V Semester**

<b>5EX4-21: Microprocessors Lab</b>	
<b>Credit:1.5</b>	<b>Max Marks:100(IA:60,ETE: 40)</b>
<b>0L+0T+3P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:** Upon successful completion of the course, students will be able to

**CO-1:** Write assembly language programs for 8085 microprocessor

**CO-2:** Understand the interfacing of peripherals with 8085 microprocessors

**CO-3:** Learn programming concepts of 8051 microcontroller

**CO-4:** Implement 8051 interfacing with peripherals

**CO-5:** Application of microprocessor, working on mini projects.

S. No .	List of Experiments	Hours
1	Study the hardware, functions, memory structure, Instruction set and operation of 8085 microprocessor kit.	3
2	Write an assembly language program to Add/Subtract two 8-bit/16-bit number.	1.5
3	To perform multiplication and division of two 8 bit numbers using 8085.	1.5
4	Write an assembly language program to Data transfer/Exchange from one memory block to another in forward and reverse order.	3
5	To write a program to arrange an array of data in ascending and descending order.	1.5
6	To find the largest and smallest number in an array of data using 8085 instruction set.	1.5
7	Write a program using 8085 Microprocessor for addition and subtraction of two BCD numbers.	3
8	Write a program using 8085 Microprocessor to generate square and triangular wave.	3
9	Write an assembly language program for displaying the decimal numbers in 7 Segment display using Microcontroller	3
10	Write an assembly language program for interfacing stepper motor with 8051.	3
11	Write an assembly language program to interface relay with 8051	3
<b>Total</b>		<b>27</b>

**III Year V Semester****5EX4-22: Control System Engineering Lab****Credit:1.5****Max Marks:100(IA:60,ETE: 40)****0L+0T+3P****EndTermExams:3 hrs.****Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Obtain the step, ramp and impulse responses of first and second order control systems.**CO-2:** Design the lag, lead and lag-lead controllers.**CO-3:** Draw the frequency responses plot of a given control system.**CO-4:** Analysis the stability of a system using Bode plot, Nyquist plot and Root-loci.**CO-5:** Design P, PI and PID controllers.

S. No.	List of Experiments	Hours
1.	Introduction to MATLAB computing control software and its control tools.	3
2.	To design a first order R-C circuit and observe its step, ramp and impulse response.	3
3.	Plot step, ramp and impulse response of a given second order control system. Take different values of damping ratio $\delta$ and natural frequency of oscillations $\omega_n$ .	3
4.	Plot and examine the frequency response of following compensating networks and find out corner frequencies. (a) Lag Network (b) Lead Network and (c) Lag-lead Network	3
5.	Draw the bode plot for a second order transfer function and observe the Gain cross—over frequency, Phase cross-over frequency, Gain Margin and Phase-Margin.	3
6.	Check for the stability of a given closed loop system using Nyquist plot.	3
7.	Examine the stability of a given transfer function of a control system using the root-loci.	3
8.	Design P, PI and PID controllers for the given specifications and calculate $K_p$ , $K_i$ and $K_d$ for them.	3
9.	Draw and study the characteristics of AC servomotor.	3
<b>Total</b>		<b>27</b>



**III Year V Semester**

<b>5EX4-23: Digital Electronics Lab</b>	
<b>Credit:1.5</b>	<b>Max Marks:100(IA:60,ETE: 40)</b>
<b>0L+0T+3P</b>	<b>EndTermExams:3 hrs.</b>

**Course Outcomes:**

Upon successful completion of the course, the students will be able to:

**CO-1:** Define different types of logic gates, identify their ICs and verify their truth table.

**CO-2:** Design various adders and subtractors.

**CO-3:** Realization of multiplexers using logic gates.

**CO-4:** Design and counters and shift registers

**CO-5:** Realization of combinational and sequential circuits in VSM and VHDL.

S. No.	Name of Experiments
1.	Realization of Basic/ Exclusive Logic Gates using Universal Logic Gate.
2.	Verification of operation of Full Adder and Full Subtractor.
3.	Design & verification of 4-bit binary adder/subtractor using binary adder IC.
4.	Realization of operation of full adder and full subtractor using IC 74151/74153 MUX.
5.	Design & verification of full adder and full subtractor using an inverted output 3 to 8 line decoder.
6.	Design and verification of operation of a BCD Adder using IC 7483.
7.	Realization of 4 X 1 MUX using basic logic gates.
8.	Verification of operation of BCD to Seven segment code conversion using IC 7447.
9.	Verification of Truth Tables of SR, D and Master Slave JK Flip flops.
10.	Design of BCD ripple counter.
11.	Design of Universal Shift Register.
12.	Implementation of Basic Combinational and sequential circuits using VSM (Virtual System Modeling)
13.	Implementation of Basic Combinational and sequential circuits using VHDL